



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of )  
Eliyahou Harari et al. )  
Serial No.: 08/407,916 )  
Filed: March 21, 1995 )  
For: FLASH EEPROM SYSTEM )  
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)

Group Art Unit: 2511  
Examiner:  
Joseph A. Popek

Hon. Commissioner of  
Patents and Trademarks  
Washington, D.C. 20231

San Francisco, California  
April 5, 1996

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231 on April 5, 1996.

Lois Simón Petipas

 4/5/96  
Signature Date

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir/Madam:

The following Form 1449 and copies of each cited document is being filed herewith as a Disclosure Statement submitted for the Examiner's consideration.

The cited documents are listed in two groups:

**I. Data Sheets of products referred to in the specification**

1. 27F256 256K (32K x 8) CMOS FLASH MEMORY, DATA SHEET, Intel Corporation, pp. 1-21.
2. 48F512 512K FLASH EEPROM - PRELIMINARY DATA SHEET", SEEQ Technology Incorp., pp. 2-1 - 2-24.

II. Pleadings files in the Matter of Certain Flash Memory Circuits and Products Containing Same; International Trade Commission, ITC Investigative No. 337-TA-382. (SanDisk Corporation, assignee of the above-captioned application and the parent issued U.S. Patent No. 5,418,752 ('752) is seeking temporary relief before the ITC against Samsung importing memory products that infringe claims in '752. A Compliant and Notice of Investigation was filed by SanDisk.)

✓ 1. SAMSUNG'S RESPONSE TO THE COMPLAINT AND NOTICE OF INVESTIGATION (2/9/96)

Attachments of interest include:

a) Invalidity Claim Chart For U.S. Patent No. 5,418,752 (Tab 1, pp. 1-3) in which the following three references are relied on:

(i) Mitsuishi U.S. Patent No. 4,931,997

(ii) Bill article "A Temperature- and process-tolerant 64K EEPROM"

(iii) Cricchi U.S. Patent No. 4,099,069

These three references have been listed in the PTO-1449 form and copies are enclosed.

✓ 2. SAMSUNG RESPONDENTS' MOTION FOR SUMMARY DETERMINATION THAT CLAIM 1 OF U.S. PATENT NO. 5,418,752 IS ANTICIPATED BY THE PRIOR ART AND FOR ENTRY OF AN ORDER DENYING SANDISK'S MOTION FOR TEMPORARY RELIEF (2/26/96)

Attachments of interest include:

a) Declaration of Dr. Jonathan Allen (EXHIBIT 2) to provide expert testimony for Samsung, especially with respect to the three references cited in II.1.a).

✓ - EXHIBIT 2B is a copy of the Mitsubishi U.S. Patent No. 4,931,997.

✓ - EXHIBIT 2C is another copy of Claim Chart For U.S. Patent No. 5,418,752.

✓ - EXHIBIT 2D is a copy of the Bill article "A Temperature- and process-tolerant 64K EEPROM".

✓ - EXHIBIT 2E is a copy of Cricchi U.S. Patent No. 4,099,069

✓ - EXHIBIT 3 is another copy of Claim Chart For U.S. Patent No. 5,418,752.

3. SANDISK'S MEMORANDUM IN OPPOSITION TO RESPONDENTS' MOTION FOR SUMMARY DETERMINATION THAT CLAIM 1 OF U.S. PATENT NO. 5,418,752 IS ANTICIPATED BY THE PRIOR ART. (3/19/96)

Attachments of interest:

a) Declaration of Dr. Denis McGreivy for providing expert testimony in support of SanDisk

b) Declaration of James C. Yoon, attorney for SanDisk

4. RESPONSE OF THE COMMISSION INVESTIGATIVE STAFF TO SAMSUNG'S MOTION FOR SUMMARY DETERMINATION THAT CLAIM 1 OF THE '752 PATENT IS ANTICIPATED BY THE PRIOR ART.

Consideration of each of these documents by the Patent Examiner, and the making of each of them of record in the file of this application, are respectfully requested.

Respectfully submitted,

Dated: April 5, 1996

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